

Innovative Integration

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PMC/XMC FPGA Cards

Speed, fidelity, flexibility - The VelociaPMC family integrates ultra-fast signal capture, generation, and co-processing on an advanced PMC architecture. Each card combines new generation analog devices with large user-reconfigurable Virtex-II Pro FPGA, ample DDR memory, low jitter clocks/triggers on a 64/66 PCI with a private JN4 64-bit user I/O port, and a XMC 4-lane Rocket I/O (per VITA 42) that connects straight to the FPGA of our Velocia cPCI boards or other carriers. This ultimate connectivity allows for rapid deployment of the most advanced systems in SDR, signal intelligence, RADAR, and radio test equipment.

The family of cards includes SIO Module Dual 1 Gbps Tx/Rx Serial I/O Ports, DR Module 16 Channel Digital Receiver Card, UWB Ultrawide Digital Receiver, and TX Digital Transmitter.



FEATURES:

- SIO C6416 DSP, 1 GHz – two PowerPC 600 MHz – Xilinx Virtex II Pro – PCI (64/66) XMC (four lanes @ 2.5 Gbps per lane)
- TX four D/A, 16-bit, 1 GHz – two PowerPC, 600 MHz – Virtex II Pro – PCI (64/66) – XMC (four lanes @ 2.5 Gbps per lane)
- UWB two A/D, 12-bit, 250 MHz – two PowerPC, 600 MHz – Virtex II Pro – PCI (64/66) – XMC (four lanes @ 2.5 Gbps per lane)
- DR (Digital Receiver) four A/D, 14-bit, 125 MHz – two PowerPC, 600 MHz – Virtex II Pro – PCI (64/66) – XMC four lane
- Full FPGA dev system (VHDL, MatLab/Simulink) supported by MatLab/Simulink
- Advanced SW and firmware demo programs, with external trigger control

For more information, contact: nhenderson@innovative-dsp.com

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